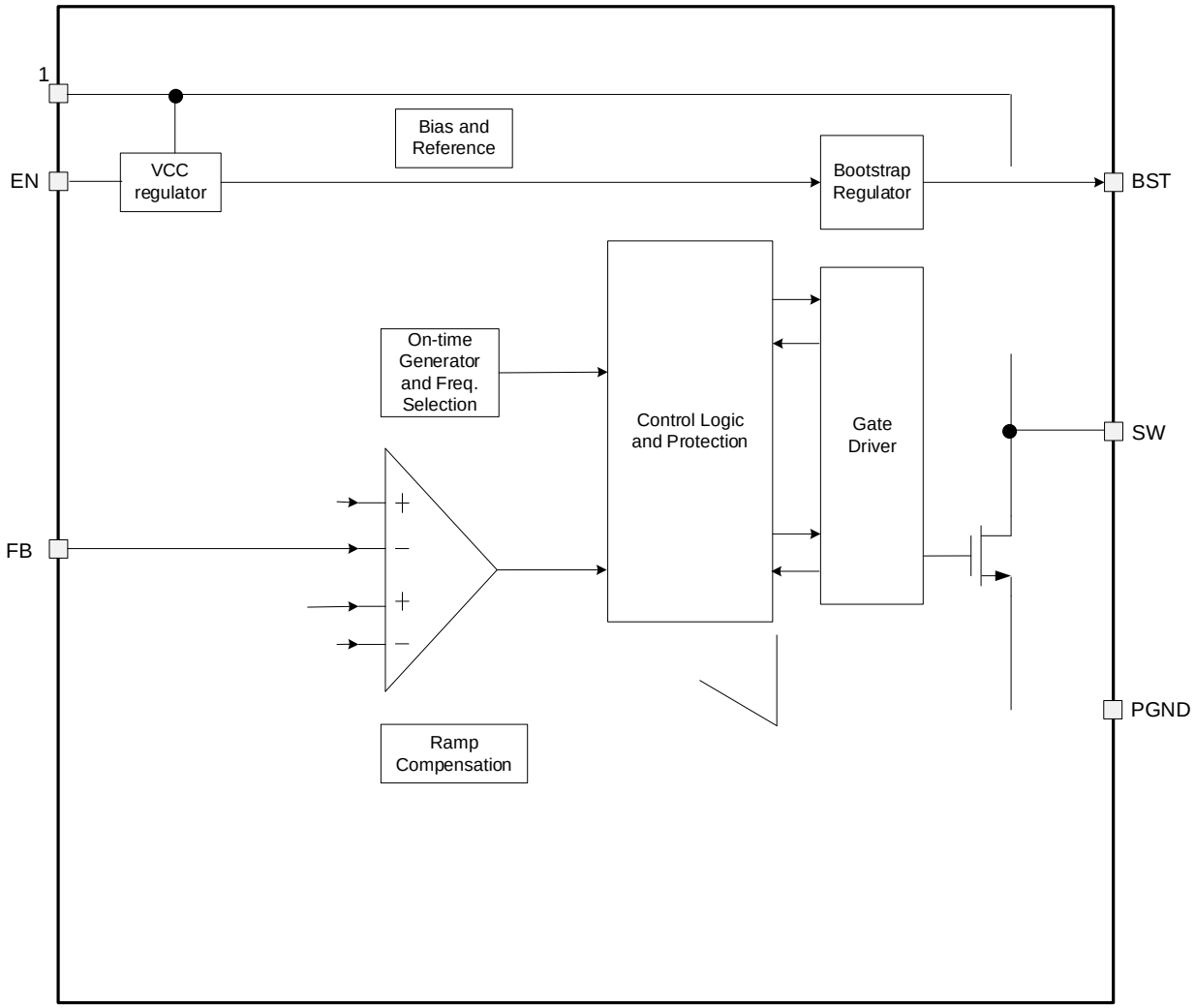
REVISION HISTORY

DEVICE ORDER INFORMATION

ORDERABLE DEVICE	PACKAGING TYPE	STANDARD PACK QTY	PACKAGE MARKING

SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Protection						

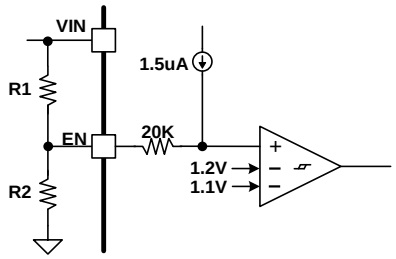
FUNCTIONAL BLOCK DIAGRAM



OPERATION

Overview

Enable and Under Voltage Lockout Threshold



Output Voltage

Switching Frequency Selection

Table 2. FSEL Pin Set-up for Switching Frequency Selection

APPLICATION INFORMATION

Typical Application

Design Parameters

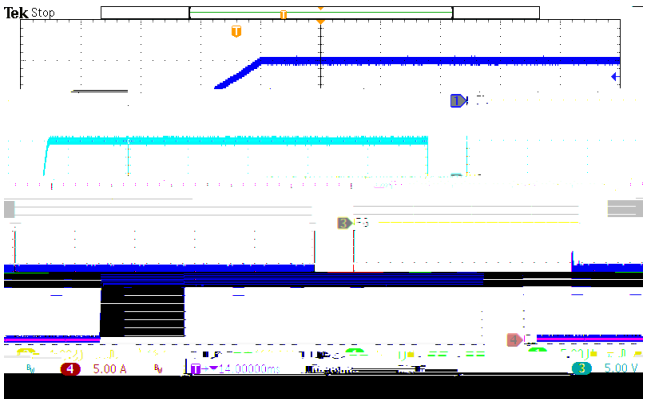
Design Parameters	Example Value

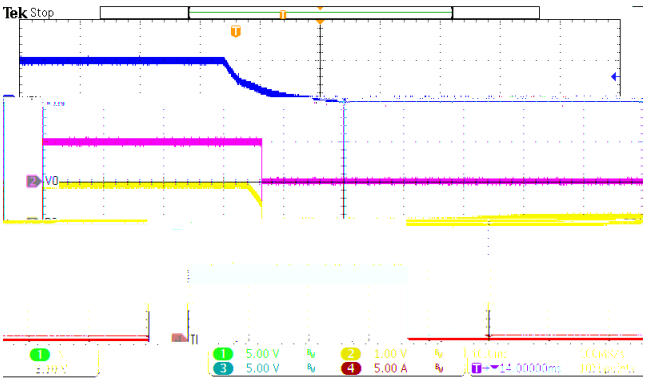
Input Capacitor Selection

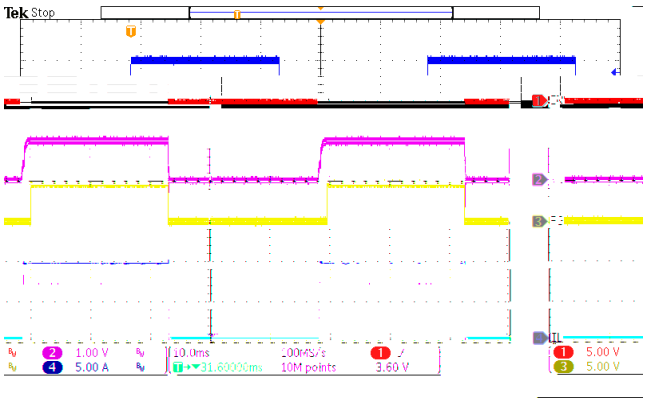
Inductor Selection

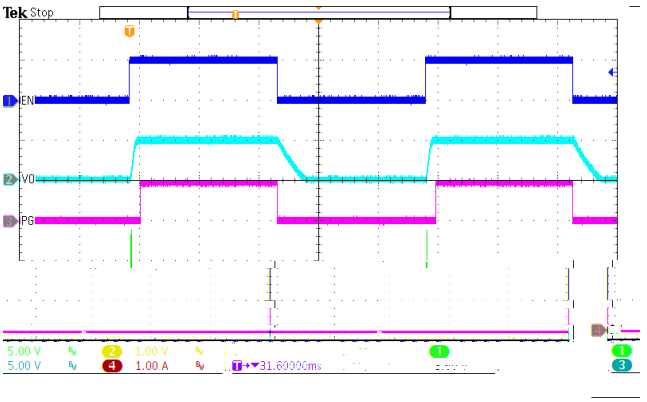
Output Capacitor Selection

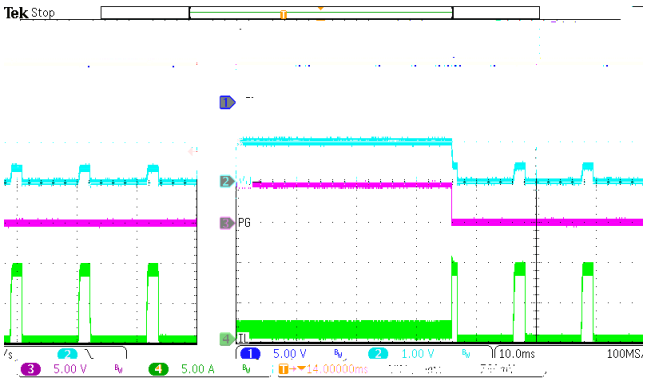
Application Waveforms

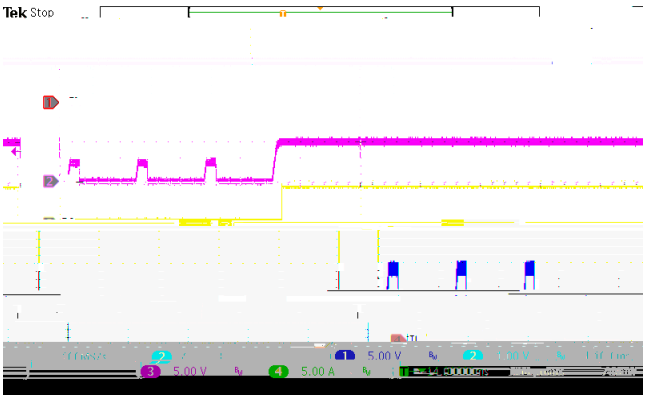








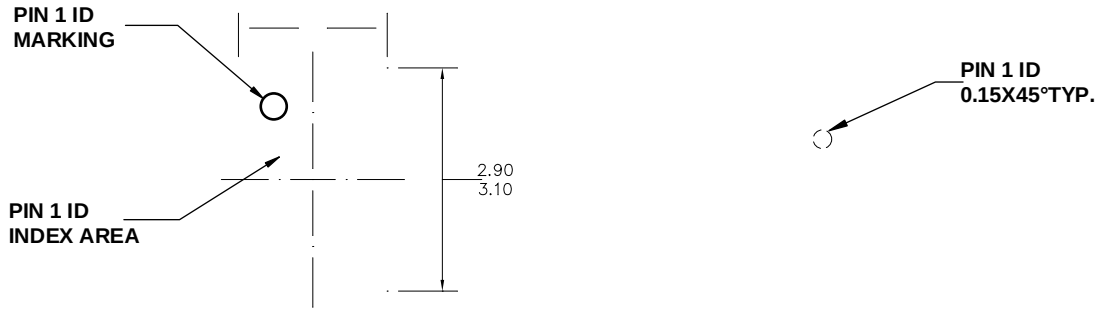




Application Waveforms(Continued)

PACKAGE INFORMATION

QFN -12 (2mm x 3mm)



1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) LEAD COPLANARITY SHALL BE 0.08 MILLIMETERS MAX.
3) JEDEC REFERENCE IS MO-220.
4) DRAWING IS NOT TO SCALE.