

Low IQ mode quiescent current vs. Vin

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.
Revision 0.8: Preliminary Specification.
Revision 0.81: Update PACKAGE INFORMATION.

ORDERABLE DEVICE	PACKAGIN G TYPE	STANDARD PACK QTY	PACKAGE MARKING	PINS	PACKAGE DESCRIPTION
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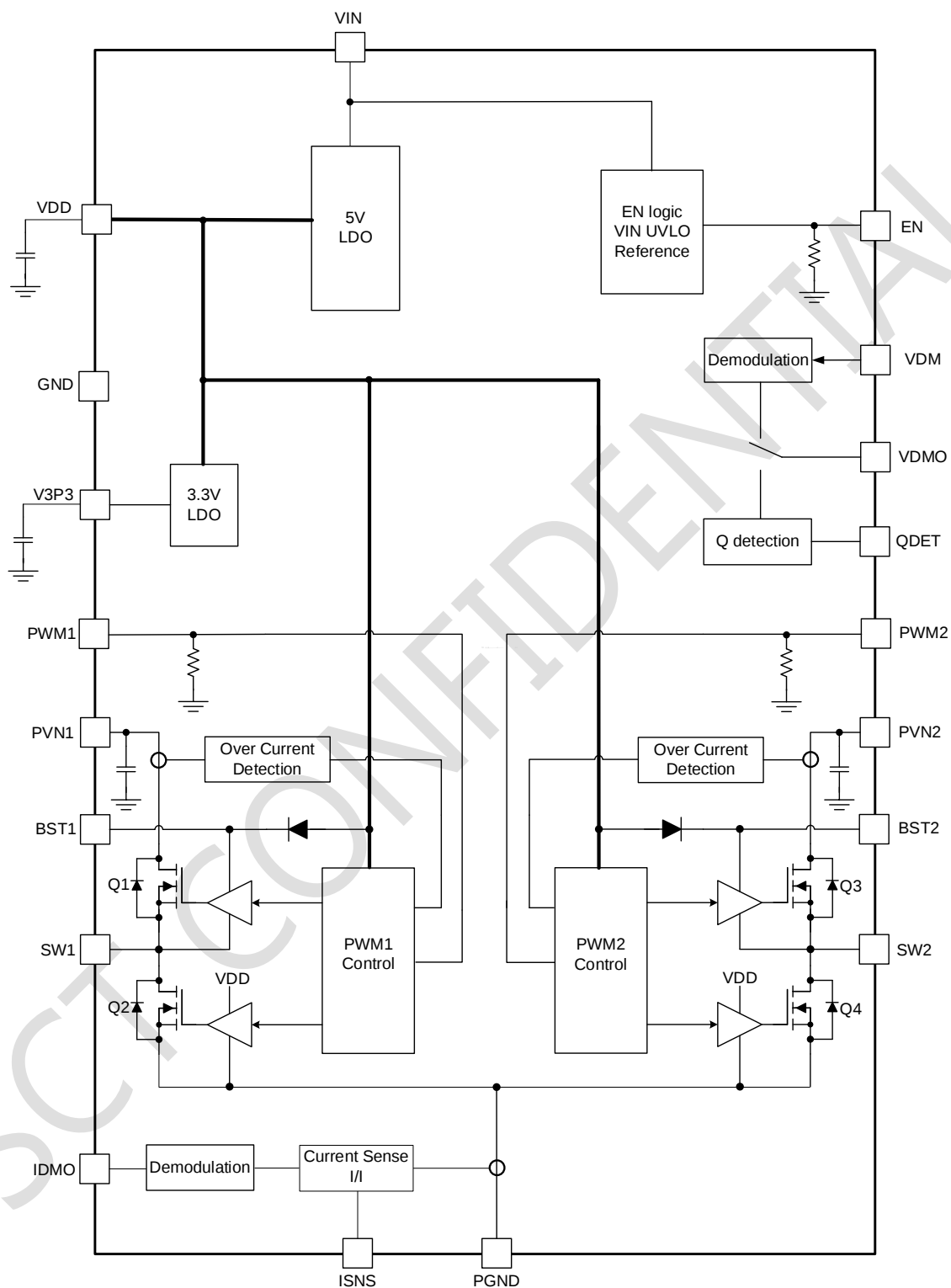


Figure 8. Functional Block Diagram

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Thermal Shutdown

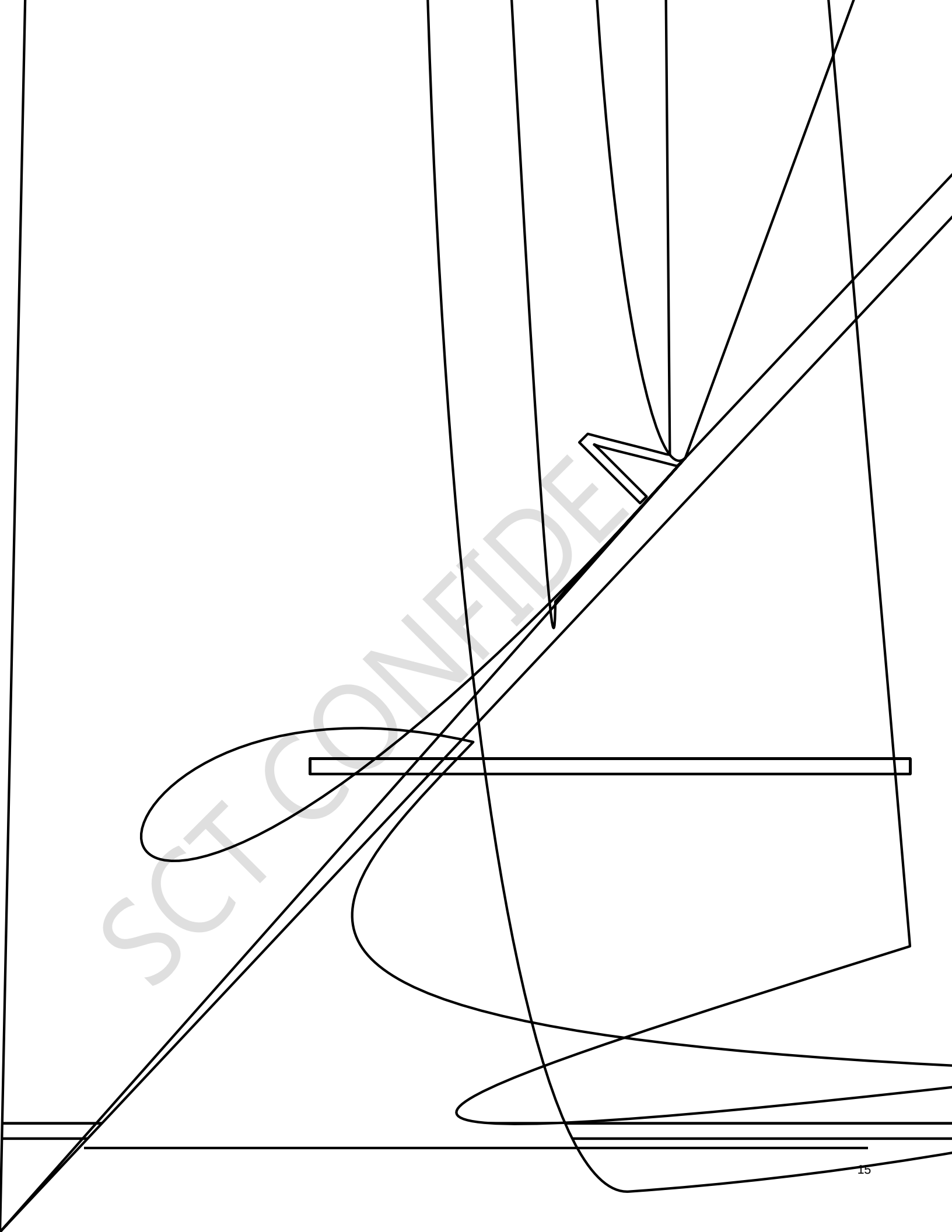
The SCT63152A protects the device from the damage during excessive heat and power dissipation condition. Once the junction temperature exceeds 160C, the thermal sensing circuit stops full bridge of 4-MOSFETs' working. When the junction temperature falls below 125C, then

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